Candidate for Chair

Andrew C. Myers
Cornell University, Ithaca, NY, USA

BIOGRAPHY

Academic Background:
Ph.D., Massachusetts Institute of Technology, 1999, Electrical Engineering & Computer Science.

Professional Experience:
Professor, Cornell University, Ithaca, NY, USA, 1999 – 2024;
Member of Technical Staff, Silicon Graphics, Inc., Mountain View, CA, USA, 1988 – 1992.

Professional Interest:
Programming Languages, Computer Security, Architecture, Computer Systems, Games and Education.

ACM Activities:
Editor in Chief, ACM TOPLAS, 2016 – 2022;
Program Committee Chair, POPL 2018, ACM SIGPLAN, 2017 – 2018;
General Chair, POPL 2023, ACM SIGPLAN, 2015 – 2016;
Member and Chair (2016), ACM Doctoral Dissertation Committee, 2014 – 2017.

Membership and Offices in Related Organizations:
Co-editor in chief, Journal of Computer Security, 2013 – 2016.

Awards Received:
Distinguished Paper Award, ACM CCS, 2023;
Best Paper Award, IEEE Security and Privacy, 2021;
Distinguished Paper Awards, ACM PLDI, 2015;
Best Paper Award, ACM SOSP, 2007.

STATEMENT

The SIGPLAN community has been, for me, a wonderful environment and community for doing research and exchanging ideas. This research community continues to have a tremendous amount to offer to the rest of computer science. The clean abstractions and powerful technologies of the PL area are needed to make future computer systems reliable, secure, and fast as the scale and complexity of these systems continue to explode. I want to help us to remain a welcoming, constructive community that is dedicated to excellence.

Suresh Jagannathan
Purdue University, West Lafayette, IN, USA

BIOGRAPHY

Academic Background:
Ph.D., Massachusetts Institute of Technology, 1989, Computer Science

Professional Experience:
Samuel D. Conte Professor, Purdue University, West Lafayette, IN, 2018 – Present;
Amazon Faculty Scholar, Amazon, Inc, Seattle, WA, 2019 – 2023;
Program Manager, Defense Advanced Research Projects Agency, Washington DC, 2013 – 2016.

Professional Interest:
Program Verification, Distributed and Concurrent Systems, Program Synthesis, Type Systems, Functional Programming

ACM Activities:
Associate Editor, ACM Transactions on Programming Languages and Systems, 2021 – Present;
POPL Steering Committee Chair, ACM SIGPLAN (POPL), 2014 – 2015;
General Chair, ACM SIGPLAN (POPL), 2013;
Steering Committee, ACM SIGPLAN (TRANSACT), 2006 – 2014.

Awards Received:
Distinguished Paper Award (PLDI), 2023;
Distinguished Paper Award (PLDI), 2019;
Distinguished Paper Award (PLDI), 2018;
Distinguished Artifact Award (OOPSLA), 2018.

STATEMENT

I am honored to be nominated to serve as Chair of SIGPLAN. SIGPLAN has been my intellectual home and the focus of many of my activities for the past 30+ years. I have served on the program committees and published papers in all of the major SIGPLAN conferences (ICFP, OOPSLA, PLDI, and POPL) multiple times. I am an Associate Editor for TOPLAS and was the general chair for POPL. Additionally, I was a Program Manager in DARPA's Information Innovation Office (I2O), responsible for creating and managing a number of programs that had

direct, tangible impact on the various research activities pursued by SIGPLAN members. I am pleased to see that SIGPLAN remains a vibrant and growing community. If elected, I hope to continue to strengthen the interaction between SIGPLAN conferences and PACM, ensure that artifact evaluation remains an important facet of research publications, foster new mentoring opportunities, and unify major SIGPLAN conference submission requirements, procedures, and support infrastructure. There also remains a significant opportunity for SIGPLAN to strengthen industry engagement with respect to conference sponsorship and related activities. Exploring outreach activities to ACM student chapters at universities worldwide is another initiative I hope to pursue.

Candidate for Vice Chair

Anders Møller
Aarhus University, Aarhus, Denmark

BIOGRAPHY

Academic Background:
Ph.D., Aarhus University, 2002, Computer Science.

Professional Experience:
Vice Head of Department, Aarhus University, Aarhus, Denmark, 2020 – Present;
Professor, Department of Computer Science, Aarhus University, Aarhus, Denmark 2017 – Present;
Associate Professor, Department of Computer Science, Aarhus University, Aarhus, Denmark, 2006 – 2017.

Professional Interest:
Program Analysis, Automated Testing.

ACM Activities:
Vice-Chair, SIGPLAN, 2021 – 2024;
Associate Editor of ACM TOPLAS, SIGPLAN, 2018 – 2024;
Associate Editor of ACM TOSEM, SIGSOFT, 2020 – 2027;
Associate/Area Chair of PLDI 2024 and OOPSLA 2024, SIGPLAN, 2024.

Membership and Offices in Related Organizations:
Vice-Chair, IFIP Working Group 2.4, 2015 – Present.

Awards Received:
Elite Research Prize, Denmark, 2020;
ACM SIGPLAN Distinguished Paper Award at OOPSLA, 2017;
ACM SIGPLAN PLDI Distinguished Reviewer Award, 2017;
ACM SIGSOFT Distinguished Paper Award at ASE, 2022.

STATEMENT

I have been an ACM member since 2003, and I have served on several SIGPLAN conference program committees. As current Vice-Chair, I am familiar with the procedures and policies of ACM and SIGPLAN, and I am honored to be asked to continue another term. If re-elected, I will work to strengthen coordination and exchange of best practices between our conferences and to improve our guidelines for conference and workshop organizers.

Candidate for Vice Chair

Mira Mezini
Technical University of Darmstadt, Germany

<u>BIOGRAPHY</u>
No biographical information and/or statement submitted.

Candidate for Member at Large

Marco Gaboardi
Boston University, MA, USA

BIOGRAPHY

Academic Background
Ph.D., University of Torino - INPL Nancy, 2007, Computer Science.

Professional Experience:
Associate Professor, Boston University, MA, USA, 2021 – Present;
Assistant Professor, Boston University, MA, USA, 2019 – 2021;
Assistant Professor, University at Buffalo - SUNY, NY, USA, 2016 – 2019.

Professional Interest:
Programming Languages, Logics and Semantics, Probabilistic Reasoning,
Data Privacy, Security.

ACM Activities:
General Chair, ACM International Conference on Functional Programming, 2024;
Conference Chair, ACM/IEEE Symposium on Logic in Computer Science, 2023;
Colocated Events Chair, ACM SIGPLAN Symposium on Principles of Programming
Languages, 2018 – 2019.

Membership and Offices in Related Organizations:
Steering Committee Chair, Workshop on Programming Languages and Analysis for
Security, 2018 – 2022;
Co-Organizer, Oregon Programming Language Summer School, 2022;
Steering Committee Member, Workshop on the Theory and Practice of Differential
Privacy, 2015 – 2023.

Awards Received:
Caspar Bowden Award for Outstanding Research in PETS, 2019;
Google Faculty Research Award, 2019;
University at Buffalo CSE Early Career Researcher of the Year, 2017.

STATEMENT

I am honored by the perspective of representing the programming language community
as a SIGPLAN Member at Large. I believe that SIGPLAN has helped our community to
grow and thrive, and I look forward to working to ensure its continued success in the
future.

As a Member at Large, I would focus on promoting initiatives aimed at increasing the participation in SIGPLAN activities and events of communities that are traditionally underrepresented, such as the ones in Asia, Latin America, Africa, and Oceania. I would focus on promoting initiatives aimed at increasing SIGPLAN support for local events as a way to more directly involve and impact local communities. I would also focus on strengthening activities and initiatives for students, making sure that more students from diverse backgrounds are supported by SIGPLAN.

As a Member at Large, I would also support SIGPLAN events and initiatives aimed at strengthening the role of programming languages in other disciplines or research areas, for example, through the support of events across multiple disciplines.

Neel Krishnaswami
University of Cambridge, UK

BIOGRAPHY

Academic Background:
Ph.D., Carnegie Mellon University, 2012, Computer Science.

Professional Experience:
Professor, University of Cambridge, UK, 2016 – Present;
Lecturer, University of Birmingham, UK, 2013 – 2016;
Postdoctoral researcher, Max Planck Institute for Software Systems, Saarbrücken, DE, 2011 – 2013.

Professional Interest:
Programming Language Design, Denotational Semantics and Category Theory, Verification and Separation logic, Proof assistants, Type Theory and Type Systems.

ACM Activities:
Associate Editor, TOPLAS, 2023 – Present;
Program Committee Member, POPL 2014/22, ICFP 2015/18/20/22, OOPSLA 2022/23, PLDI 2019/24, 2014 – Present.

Membership and Offices in Related Organizations:
Member, IFIP Working Group 2.8 on Functional Programming , 2021 – Present.

Awards Received:
European Research Council Consolidator Grant, "TypeFoundry", 2021;
POPL Distinguished Paper Award, 2020;
PLDI Distinguished Paper Award, 2019.

STATEMENT

I am happy to be nominated for SIGPLAN Member at Large. Programming languages has been my scholarly home for over two decades. I hope to maintain our community institutions where they work well, and to help build consensus for improvements where they need to work better.

One of the most important institutional issues our community faces is the accessibility of our work.

Directly, I would like to see our papers themselves become more accessible. There are a variety of things we could do: working with the ACM to improve its web-native publication pipeline (TAPS) to handle papers with many equations and diagrams adequately, improving LaTeX backends to generate accessible PDFs, and even to publish document sources on the ACM Digital Library.

Another accessibility issue is rising conference costs. Over the last few years, economic conditions (such as high inflation and higher hotel rates) have paired with increased expectations (such as videoconferencing and subsidizing open access fees), resulting in much higher registration fees. I would like to see if there is anything SIGPLAN can do to support our conference organisers in keeping these costs under control. This will help students and researchers from more institutions participate in PL community activities.

Madan Musuvathi
Microsoft Research, Redmond, WA, USA

BIOGRAPHY

Academic Background:
Ph.D., Stanford University, 2004, Computer Science.

Professional Experience:
Partner Research Manager, Microsoft Research, Redmond, WA, USA, 2018 – Present;
Senior Principal Researcher, Microsoft Research, Redmond, WA, USA, 2014 – 2018;
Principal Researcher, Microsoft Research, Redmond, WA, USA, 2008 – 2014.

Professional Interest:
Programming Languages, Systems, Machine Learning.

ACM Activities:
ASPLOS Program Co-Chair, 2022 – Present;
ASPLOS Steering Committee, 2023 – Present;
PLDI D&I Co-Chair, 2022 – 2024.

Awards Received:
CAV Award, 2023.

STATEMENT

I have actively participated in the SIGPLAN community for the past two decades. As the manager of the PL group at Microsoft Research, Redmond, I encourage and oversee the professional activities of several active members of the PL community. I have served as the D&I co-chair of PLDI for 2022/2023 and helped organize events to bring under-represented communities together.

I am very passionate about making our community diverse and inclusive. Building on the recent progress we have made, we can further increase the representation across all dimensions of gender, race, and geography. If we believe that innate (PL) talent is uniformly distributed across the world, then the ideal we should shoot for is to make our community a random sample of the world population - i.e. ~50% women, ~16% from Africa, and so on. While this ideal might seem impossible, small steps in our organization and outreach should help us progressively work towards this ideal. I will build on the progress we have made through SIGPLAN-M and PLMW to identify and

promote young talent independent of where we find them and expand the scope of our mentoring opportunities to under-represented regions/communities.

Santosh Ganapati Nagarakatte
Rutgers University, New Brunswick, NJ, USA

BIOGRAPHY

Academic Background:
Ph.D., University of Pennsylvania, 2012, Computer Science.

Professional Experience:
Professor, Rutgers University, New Brunswick, NJ, USA, 2023 – Present;
Undergraduate Program Director, Rutgers University, New Brunswick, NJ, USA, 2020 –Present;
Visiting Fellow, Princeton University, Princeton, NJ, USA, 2016.

Professional Interest:
Formal Verification, Compilers, Computer Architecture, Math Libraries, Computer Security.

ACM Activities:
Vice-Chair, ASPLOS, , 2023 – 2024;
General Chair, VEE conference , 2020 – 2021;
Sponsorship Chair, PLDI, 2014 – 2016;
Program Co-Chair, WODA, 2014.

Membership and Offices in Related Organizations:
SIGPLAN Track Chair, SIGPLAN/PLDI, 2022;
Program Committee Member, PLDI/POPL/ASPLOS/OOPSLA, 2013 – Present.

Awards Received:
POPL 2022 Distinguished Paper Award, 2022;
CGO 2022 Best Paper Award, 2022;
PLDI 2021 Distinguished Paper Award, 2021;
PLDI 2015 Distinguished Paper Award, 2015.

STATEMENT

I have been a member of SIGPLAN for the last 15 years starting as a student and now as a faculty member in a large public university. I have participated in the SIGPLAN affiliated conferences (PLDI/POPL/OOPSLA/ASPLOS) in various capacities: as an author, as a reviewer on the technical program committee, sponsorship chair, SIGPLAN track chair, and program vice-chair.

As a member of the SIGPLAN Executive Committee, I would like to work towards:

1. Improving the collaboration with other closely aligned areas of computer architecture and computer systems.

2. Improving and streamlining the process for selecting SIGPLAN research highlight papers and eventually making nominations for CACM research highlights.

3. Contributing to SIGPLAN's efforts to build an inclusive community.

Candidate for Member at Large

Brigitte Pientka
McGill University, Montreal, Canada

BIOGRAPHY

Academic Background:
Ph.D., Carnegie Mellon University, 2003, Computer Science.

Professional Experience:
Full Professor, McGill University, Montreal, Canada, 2020 – Present;
Associate Professor, McGill University, Montreal, Canada, 2009 – 2020;
Assistant Professor, McGill University, Montreal, Canada, 2003 – 2009.

Professional Interest:
Theory of Programming Languages, Type Theory, Type Systems, Automated Reasoning.

ACM Activities:
Program Committee Chair, ICFP'24, 2023 – Present;
Program Committee Co-chair, CPP'23 and CPP'24, 2022 – 2024;
General Conference Chair, POPL'20, 2019 – 2020.

Membership and Offices in Related Organizations:
Member of Steering Committee, POPL, 2017 – 2020;
Member of Steering Committee, LICS, 2022 – Present;
Member of Steering Committee, ESOP, 2017 – Present.

Awards Received:
PPDP Test of Time Award, 2018;
Humboldt Research Fellow, 2017.

STATEMENT

It is an honor to be asked to run for Member at Large on the SIGPLAN Executive Committee.

I strongly believe in building an inclusive and equitable research community where everyone can thrive. However, there are systemic challenges that make this difficult: Inflation and the rising cost of publishing and travel pose significant strain on available financial resources that researchers have at their disposal. In addition to being costly and time-consuming, conference-related travel generates a substantial carbon footprint.

SIGPLAN has consistently led the way before with ideas such as double-blind reviewing, open access (via PACMPL), mentoring activities, PAC funding, and confronting climate change. As part of the SIGPLAN EC, I will continue to push towards expanding these activities.

At the same time, I believe it is important to set a vision for diversity and inclusion, formulate clear goals that we, as a community, want to achieve, and assess where we currently stand. This also requires us to be more transparent about current practices, conference-related costs, and our carbon footprint.

If elected, it will be my privilege to contribute towards developing new strategies in how we organize and run conferences to make them more sustainable, equitable, and inclusive.

Candidate for Member at Large

Ruzica Piskac
Yale University, New Haven, CT, USA

BIOGRAPHY

Academic Background:
Ph.D., EPFL, Switzerland, 2011, Computer Science.

Professional Experience:
Associate Professor, Yale University, New Haven, CT, 2021 – Present;
Associate Professor on Term, Yale University, New Haven, CT, 2019 – 2021;
Assistant Professor, Yale University, New Haven, CT, 2013 – 2019.

Professional Interest:
Programming Languages, Program synthesis, Code verification, Applied cryptography,
Legal reasoning.

Membership and Offices in Related Organizations:
Steering Committee Member, FMCAD, 2021 – Present;
Steering Committee Member, VMCAI, 2019 – Present;
Steering Committee Member, SMT, 2021 – 2023.

Awards Received:
Distinguished Paper Award at the CCS, 2022.

STATEMENT

Modern research has become more and more interdisciplinary. The PL/verification
community has so much to offer to this trend. Almost every aspect of computer science
and computing benefits from formally verifying the computation. While we know that the
verification problem is undecidable in its general form, we are witnessing how the range
of programs and systems that are formally verified is constantly increasing.

My research covers a diverse range of specialties across the computer sciences
spectrum, including automated reasoning, program verification, network verification,
research in database languages, program synthesis and reactive programs, applied
cryptography, and legal reasoning. A common thread in my research is improving
software reliability and trustworthiness using formal techniques. I consider conferences
such as POPL and PLDI my home research communities, but in the past few years, I
have been publishing also at different conferences, such as CCS, SIGMOD, CS&Law.
My work has a strong formal component and applies techniques that originated from the

POPL and PLDI communities. We also enhanced those techniques because new applications lead to new research results.

If elected, my goal is to work so that my home community becomes more inclusive of other research communities, to each other's benefit.

Candidate for Member at Large

Alex Potanin
Australian National University, Canberra, Australia

BIOGRAPHY

Academic Background:
Ph.D., Victoria University of Wellington, 2007, Software Engineering.

Professional Experience:
Associate Professor, Australian National University, Canberra, Australia, 2022 – 2024;
Associate Dean (Students), Victoria University of Wellington, New Zealand, 2021 – 2022;
Lecturer/Senior Lecturer/Associate Professor, Victoria University of Wellington,
New Zealand, 2007 – 2021.

Professional Interest:
Programming Languages Design, Type Systems, Ownership and Immutability,
Capabilities and Effects, Software Engineering.

ACM Activities:
OOPSLA 2024 Review Committee Co-Chair, SIGPLAN, 2023 – 2024;
SPLASH 2022 General Chair, SIGPLAN, 2021 – 2022;
Virtualisation Co-Chair SPLASH 2020 and 2021, SIGPLAN, 2020 – 2021;
SPLASH 15,16,17, ICFP18: Publications SPLASH17,18: Workshops Chair, SIGPLAN,
2015 – 2018.

Membership and Offices in Related Organizations:
General Chair APLAS 2018, Asian Symposium on Programming Languages, 2018;
Program Co-Chair APSEC 2016, Asia-Pacific Software Engineering Conference, 2016;
Onward! and SPLASH Steering Committee Member, SIGPLAN, 2022 – 2024.

Awards Received:
ECOOP14 Distinguished Paper and ECOOP17 Distinguished Artefact, 2014;
ESEC/FSE Distinguished Paper Award, 2007;
2nd Prize and Judges Prize at ICFP Programming Contest 2005, 2003;
2nd Place in the ACM International Student Research Competition, 2003.

STATEMENT

I am honoured to be nominated for a member Member-at-Large position on ACM
SIGPLAN's Executive Committee. I have been involved in helping organise SIGPLAN
conferences since my first time as an undergraduate student volunteer at OOPSLA 2002.

I am based far away, and academics and students in my region have limited travel funding, so I appreciate the (sometimes unaffordable and rising) costs of registrations, travel, and accommodation. Remote participation is also a challenge: our time zone, like that of East Asia, is directly opposite from those in the US or EU. I hope to spend my time exploring a more accessible and affordable conference model with a clear remote participation option (ideally: easy and free for those people in different time zones who lack the financial support to take an active part in the international PL community). Having PACM include POPL, PLDI, ICFP, and OOPSLA already addresses some of the participation issues when it comes to the publishing model, but the networking and introductions problem remains unsolved. For example, some communities (like Software Engineering) choose to rotate their major conferences around the world regularly, which would allow a PACM author to choose the most affordable location in which to present.

Candidate for Member at Large

Alexandra Silva
Cornell University, Ithaca, NY, USA

BIOGRAPHY

Academic Background:
Ph.D., Radboud University Nijmegen, 2010, Computer Science.

Professional Experience:
Professor, Cornell University, Ithaca, NY, USA, 2021 – Present;
Professor, University College London, UK, 2015 – 2021;
Assistant Professor, Radboud University Nijmegen, NL, 2011 – 2015;

Professional Interest:
Automata, Formal Languages, Verification, Networks, Probabilistic Programming.

ACM Activities:
Member at Large, SIGPLAN, 2019 – Present;
Secretary, SIGLOG, 2014 – 2019.

Awards Received:
Presburger Award, 2017.

STATEMENT

Since the last election (2019) we have seen incredible change in the world with the pandemic forcing all of us to try and shift conferences online (with partial success) and fostering new initiatives like SIGPLAN-m, which has been an incredible success, and I have been honored to be part of from the early days.

More recently, the increase in costs of conference organization as well as increasing concerns on the impact our conferences have on the planet have placed an urgency in discussions and actions to change the way we do things. SIGPLAN has a key role in the steering of our main conferences, and I believe SIGPLAN must take a leading role and devise policies and actions that other SIGs will follow.

As part of the SIGPLAN EC, I will continue my work on diversity initiatives and foster a discussion on how to adapt the way we organize and run conferences to make them more sustainable, while still retaining the great sense of community that SIGPLAN built in the last decades.

Azalea Raad
Imperial College London, UK

BIOGRAPHY

Academic Background:
Ph.D., Imperial College London, 2017, Computing

Professional Experience:
Senior Lecturer (Associate Professor), Imperial College London, UK, 2022 – Present;
Lecturer (Assistant Professor), Imperial College London, UK, 2020 – 2022;
Postdoctoral Researcher, Max Planck Institute for Software Systems, Kaiserslautern, Germany, 2017 – 2020.

Professional Interest:
Weak Memory Models, Separation Logic, Bug Detection, Program Logics, Semantics.

Membership and Offices in Related Organizations:
Co-director, UK Research Institute on Verified Trustworthy Software Systems, 2022 – Present.

Awards Received:
Distinguished Paper Award at OOPSLA (ACM), 2022;
Distinguished Artifact Award at ESOP, 2022.

STATEMENT

I am honoured to be nominated for the SIGPLAN Executive Committee. SIGPLAN is my intellectual home and the focus of my academic activities. I have published numerous papers in POPL, OOPSLA and PLDI, served on the program/organisation committees of POPL, PLDI, OOPSLA and ICFP, and have organised co-located events (e.g. PLMW, TutorialFest and workshops) at POPL and PLDI. I have delivered invited talks at co-located events and am a proud recipient of the ACM SIGPLAN Distinguished Paper Award. I have additional leadership experience as the co-director of the UK Research institute on Verified Trustworthy Software Systems (VeTSS, https://vetss.org.uk).

I am delighted with the progress SIGPLAN has made in the last decade (especially regarding important EDI and outreach activities e.g. through the mentorship program and CARES initiatives). If elected, my priorities are to strengthen participation from underprivileged and marginalised backgrounds. I aim to do this by strengthening the industry engagement with SIGPLAN conferences to facilitate more extensive

sponsorships (e.g., to fund travel scholarships) and by striving for better transparency around the ACM budget/income (through registrations and publications) to save costs and to subsidize participation/registration further, where possible.

Candidate for Member at Large

Charles Chuan Zhang
HKUST, Hong Kong

BIOGRAPHY

Academic Background:
Ph.D., University of Toronto, 2007, Computer Engineering.

Professional Experience:
Professor, HKUST, Hong Kong, 2023 – 2024;
Associate Professor, HKUST, Hong Kong, 2013 – 2023;
Assistant Professor, HKUST, Hong Kong, 2008 – 2013;

Professional Interest:
Program analysis, Software engineering, Security.

ACM Activities:
ISSTA Doctoral Symposium Co-Chair, SIGSOFT, 2018 – 2019;
ICSE Student Research Competition Co-Chair, SIGSOFT, 2016 – 2017;
ICSE Proceeding's Chair, SIGSOFT, 2015 – 2016.

Awards Received:
ACM SIGPLAN OOPSLA Distinguished Paper, 2022;
ACM SIGSOFT ICSE Distinguished Paper, 2019;
ACM SIGPLAN PLDI Distinguished Paper, 2013;
ACM SIGSOFT Distinguished Ph.D. Dissertation of Student, 2013.

STATEMENT

It is an honor to be nominated to run for Member at Large of the executive committee of ACM SIGPLAN. I have been involved in ACM professionally for over two decades, actively publishing papers at ACM SIGSOFT and SIGPLAN conferences and contributing to the organization of these events, including serving on over a dozen program committees and helping with the management team. Therefore, I consider myself to be familiar with the SIG communities and the basic aspects of ACM activities. ACM has been fundamental in promoting my career as a researcher and I have the zeal and the determination to give back at a more committed level such as the role I am running for.

As a Member at Large, I will work with dedication to help SIGPLAN to face new challenges and to continue playing the critical role of promoting both research and

education. I would like to contribute more to cultural diversity and the promotion of SIGPLAN's missions locally in Asia. I will work closely with the chair and other members to ensure SIGPLAN's continued leadership in fostering programming language research.

Candidate for Member at Large

Ilya Sergey
National University of Singapore, Singapore

BIOGRAPHY

Academic Background:
Ph.D., KU Leuven, 2012, Computer Science.

Professional Experience:
Associate Professor, National University of Singapore, 2018 – Present;
Assistant Professor, University College London, UK, 2015 – 2018;
Postdoctoral Researcher, IMDEA Software Institute, Spain, 2013 – 2015.

Professional Interest:
Programming Languages, Program Verification, Program Synthesis,
Distributed Systems, Concurrency.

ACM Activities:
PLDI Steering Committee, SIGPLAN, 2022 – 2025;
POPL Steering Committee, SIGPLAN, 2022 – 2025;
ICFP Steering Committee, ISGPLAN, 2021 – 2027.

Membership and Offices in Related Organizations:
ETAPS Steering Committee, European Joint Conferences on Theory and Practice of Software,
2021 – 2025;
Executive Committee Member, Asian Association for Foundation of Software (AAFS),
2021 – 2025.

Awards Received:
ACM SIGPLAN Distinguished Paper Award (PLDI), 2023;
ACM SIGPLAN Distinguished Paper Award (PLDI), 2021;
ACM SIGPLAN Distinguished Paper Award (POPL), 2019;
Dahl-Nygaard Junior Prize, 2019.

STATEMENT

It is my honour to be nominated as a Member at Large in the 2024 ACM SIGPLAN Election.

In the past decade, I have been an active member of multiple SIGPLAN activities: I have
organised the ICFP Programming Contest in 2019; I am currently serving as a Member at Large
on the PLDI Steering Committee; I am a Publicity Chair for ICFP, and an Industry Relationship

Chair for POPL. In 2025, I will be serving as a General Chair for ICFP'25, taking place in Singapore. I believe my experience in these positions has prepared me to serve as an ACM SIGPLAN Member at Large.

As a researcher in Programming Languages affiliated with one of major academic institutions in Southeast Asia (NUS), I believe I can adequately represent concerns and interests of researchers located in this part of the world, ensuring accessibility of the SIGPLAN conferences and other ways to interact within the broader PL community. As a part of an ongoing outreach initiative by NUS, I will contribute to the efforts to improve the visibility of PL research and the opportunities it provides in SEA countries that historically have no large presence in our research field.

Candidate for Member at Large

Manu Sridharan
University of California, Riverside, CA, USA

BIOGRAPHY

<u>Academic Background:</u>
Ph.D., University of California, Berkeley, 2007, Computer Science.

<u>Professional Experience:</u>
Professor, University of California, Riverside, CA, USA, 2019 – Present;
Staff Technical Lead / Manager, Uber Technologies, Inc., Palo Alto, CA, 2017 – 2018;
Senior Staff Research Engineer, Samsung Research America, Mountain View, CA, 2013 – 2016.

<u>Professional Interest:</u>
Static program analysis, pluggable type systems, type inference, language-based security, program synthesis.

<u>ACM Activities:</u>
General Chair, SPLASH 2024, 2024 – Present;
Finance Chair, FSE 2023;
Committee Member, SIGPLAN Programming Languages Software Award Committee, 2021 – Present;
Program Committee Chair, OOPSLA 2018.

<u>Membership and Offices in Related Organizations:</u>
Program Committee Chair, ECOOP 2021;
Member, Association Internationale pour les Technologies Objets (AITO), 2021 – Present;
Senior Member, IEEE, 2022 – Present.

<u>Awards Received:</u>
Distinguished Artifact Award, ECOOP 2022;
Distinguished Reviewer Award, ICSE 2020;
Outstanding Artifact Award, OOPSLA 2013;
Best Paper Award, ASE 2003.

STATEMENT

I am honored to be considered for a Member at Large position on the SIGPLAN Executive Committee. I have been attending SIGPLAN conferences for over twenty years and have served on multiple SIGPLAN conference organizing committees,

including review committee chair for OOPSLA 2018 and general chair for SPLASH 2024. I have benefited greatly from SIGPLAN's outstanding research venues and welcoming community, and I hope to have the opportunity to give back by serving on the EC.

As a Member at Large, I would like to find ways to facilitate richer interactions between industry and academia at SIGPLAN conferences. Such interactions could spark new research directions and facilitate greater adoption of techniques from our community. I have significant experience working in both industry and academia, which I expect will help me bridge gaps and find new opportunities in this space.

I would also like to aid in efforts to make it easier and more affordable to attend SIGPLAN conferences. Deeper industry connections could help here, by increasing sponsorships. I would also like to explore greater coordination across SIGPLAN in choosing conference locations so that more community members regularly have a SIGPLAN conference hosted close to them.

Niki Vazou
IMDEA Software Institute, Madrid, Spain

BIOGRAPHY

Academic Background:
Ph.D., UC San Diego, 2017, CSE.

Professional Experience:
Associate Research Professor, IMDEA Software Institute, Madrid, Spain,
2022 – Present;
Assistant Research Professor, IMDEA Software Institute, Madrid, Spain, 2018 – 2022.

Professional Interest:
Program Verification, Functional Programming, Type Systems, Refinement Types,
Automatic Verification.

ACM Activities:
PLDI Sponsorship Co-Chair, PLDI , 2024;
Co-Chair , Haskell Symposium , 2024 – 2025;
Co-Chair Artifact Evaluation, PLDI, 2021 – 2022;
Chair Student Research Competition, POPL, 2019 – 2020.

Membership and Offices in Related Organizations:
Board Member, Haskell Foundation, 2021 – 2022;
Member, Haskell.org Committee, 2016 – 2020.

Awards Received:
ERC Starting Grant, 2022;
Best paper award at ACM SIGPLAN Conference OOPSLA, 2018.

STATEMENT

I am excited to announce my candidacy for the SIGPLAN elections. My journey with SIGPLAN began in 2012 when I attended the first PLMW at POPL. The welcoming and inclusive atmosphere of our community immediately captivated me. Beyond advancing programming language research, we prioritize effective knowledge transmission through compelling paper writing and stimulating talks, and we care about education and inclusion. What makes our community unique is the combination of foundational mathematics and cutting-edge applications in computer science.

Since 2012, I have published and served as a PC member at all four POPL, PLDI, ICFP, and OOPSLA. I have also contributed as a co-chair for Student Research Competition, Virtualisation, Sponsorship, and Artifact Evaluation in conferences. Additionally, I have organized and presented at PLMWs, in gratitude to the venue that introduced me to the scientific world.

I have enjoyed contributing to the community and aspire to collaborate with the SIGPLAN committee to further enhance our community. If elected, I will build upon the valuable work in diversity, inclusion, and mentorship already in place within our community, and I aim to strategically adapt our conferences to align with the ongoing impact of artificial intelligence, changing budget dynamics, and environmental considerations.